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APPLICANTS

Jiaw-Ren Shin, Hsin-Chu, TAIWAN;

Jian-Hsing Lee, Hsin-Chu, TAIWAN;
Shui-Hung Chen, Hsin-Chu, TAIWAN;** CONTINUING DATA*No NE PP*** FOREIGN APPLICATIONS*No NE PD*

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Foreign Priority claimed 35 USC 119 (a-d) conditions met	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance	STATE OR COUNTRY TAIWAN	SHEETS DRAWING 2	TOTAL CLAIMS 20	INDEPENDENT CLAIMS 4
Verified and Acknowledged <i>Jiaw-Ren Shin</i> Examiner's Signature	<i>PD</i> Initials				

ADDRESS

TUNG & ASSOCIATES
 Suite 120
 838 W. Long Lake Road
 Bloomfield Hills , MI
 48302

TITLE

Deep well implant structure providing latch-up resistant CMOS semiconductor product

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